## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ECTION +/11/03 MULLISH

In re the Application of: KURIHARA, Hideo et al.

Group Art Unit: 2823

Serial No.: 10/085,023

Examiner: Joannie A. Garcia

Filed: March 1, 2002

P.T.O. Confirmation No.: 8810

For: SEMICONDUCTOR MEMORY AND METHOD OF MANUFACTURE

THEREOF

RESPONSE TO THE RESTRICTION REQUIREMENT

DATED March 11, 2003

Commissioner for Patents Washington, D.C. 20231

Date: April 9, 2003

Sir:

This paper is submitted in response to the Official Action dated March 11, 2003.

In the Action, restriction is required between Group (I), Claims 1-5; and Group (II), Claims 6-9.

Applicants hereby elect the subject matter of Group (I), Claims 1-5 for prosecution in this application. This election is made without traverse, it being understood that the applicants' rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC

In the event that this paper is not timely filed, applicants hereby petition for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

In the event any additional fees are required in connection with this response, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

Michael N. Lau

Attorney for Applicant Reg. No. 39,479

MNL/alw

Atty. Docket No. 020137

Suite 1000

1725 K Street, N.W.

Washington, D.C. 20006

(202) 659-2930

23850

PATENT TRADEMARK OFFICE

H:\HOME\AWEAVER\MLAU\02\020137\04-09-03 Rest Req